

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8	"6163498"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:24
L2	242	(semiconductor adj3 memory adj5 device) same (master or (master adj3 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:26
L3	3	2 same (CSL or (column adj5 select adj5 line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:32
L4	46	2 and (CSL or (column adj5 select adj5 line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:37
L5	39	4 and (enable or disable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:27
L6	29	5 and driver\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:27
L8	28	6 and (delay\$3 or latch\$2 or register or (flip adj3 flop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:38
L10	27	8 and (controller or control\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:30

L11	26	10 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:30
L12	15	kim-byung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:30
L13	8	kim-byung-c.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:30
L15	0	ko-seung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:31
L16	0	ko-seung-b.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:31
L17	0	mills adj5 onello.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:32
L18	0	mill adj5 onello.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:32
L19	46	2 and (CSL or (column adj5 select adj5:line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:32

L20	45	19 and (delay\$3 or latch\$2 or register or (flip adj3 flop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:33
L21	29	20 and (delay\$3 or latch\$2 or register or (flip adj3 flop)) and driver\$3 and controll\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:33
L22	1	21 and 365/191.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:34
L23	2	21 and 365/230.06.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:34
L24	2	21 and 365/230.08.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:35
L25	1	21 and 365/194.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:35
L27	2319	365/194,230.06,230.08,191.ccls. and (semiconductor adj3 memory adj3 devic\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:37
L29	27	27 and (CSL or (column adj5 select adj5 line)) and controller and enable and disable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:37

L30	27	29 and (delay\$3 or latch\$2 or register or (flip adj3 flop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:39
L31	3	30 and master\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/16 07:40